

FIG. 1

200

	210A	210B	210C
	PREFIX LENGTH	NOTATION	BITS STRIPPED FROM 80 BIT INCOMING KEY
220A	49 BIT	/49	0/1 ... 0/1XXXXXXXXXX ... X
	50 BITS	/50	0/1 ... 0/1 0/1 XXXXXX ... X

	79 BITS	/79	0/1 0/1 0/1 ... 0/1 0/1 0/1 X
220B	80 BITS	/80	0/1 0/1 0/1 ... 0/1 0/1 0/1 0/1

FIG. 2

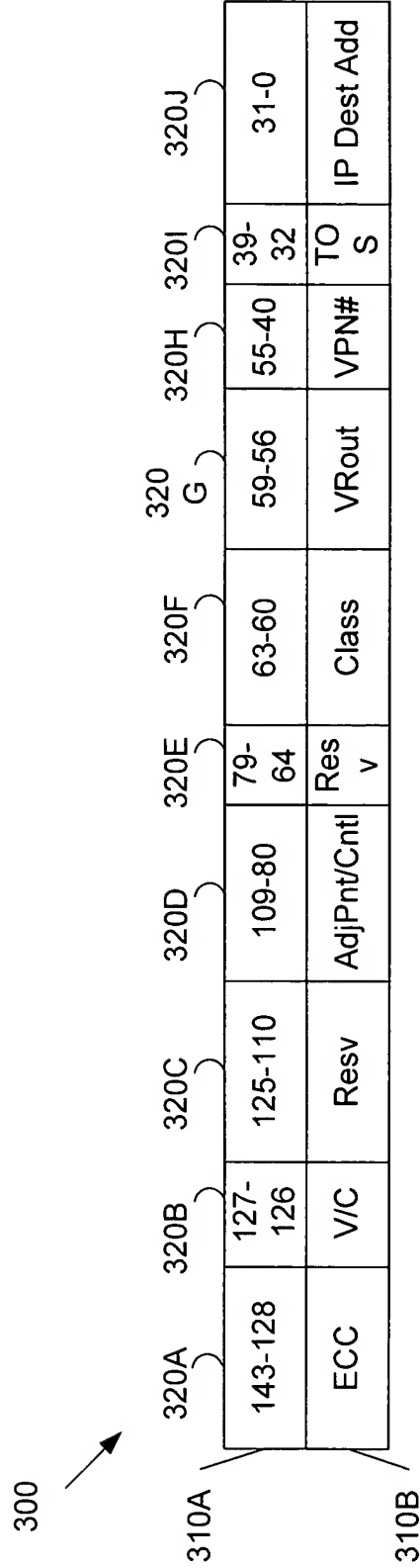


FIG. 3

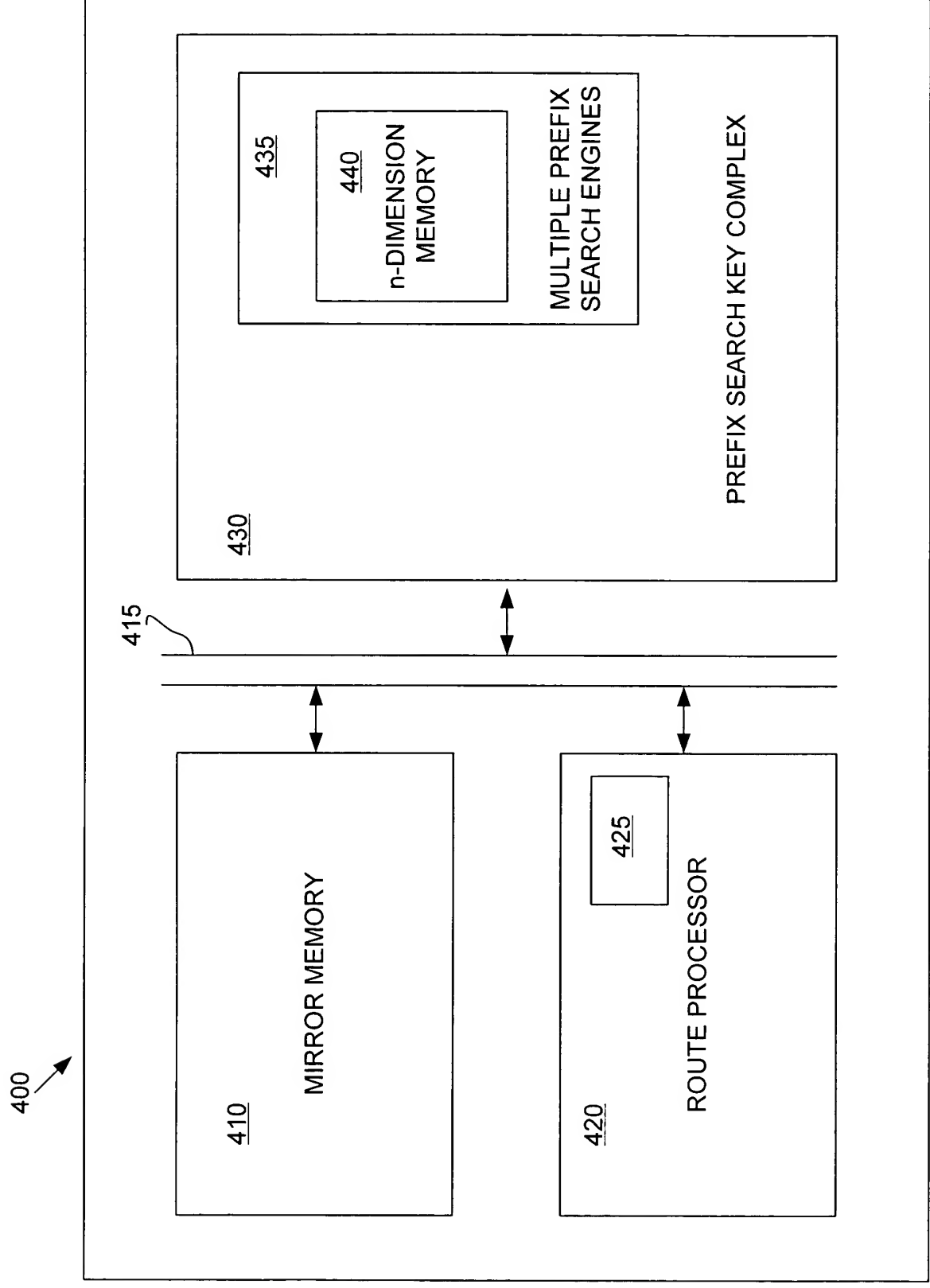


FIG. 4

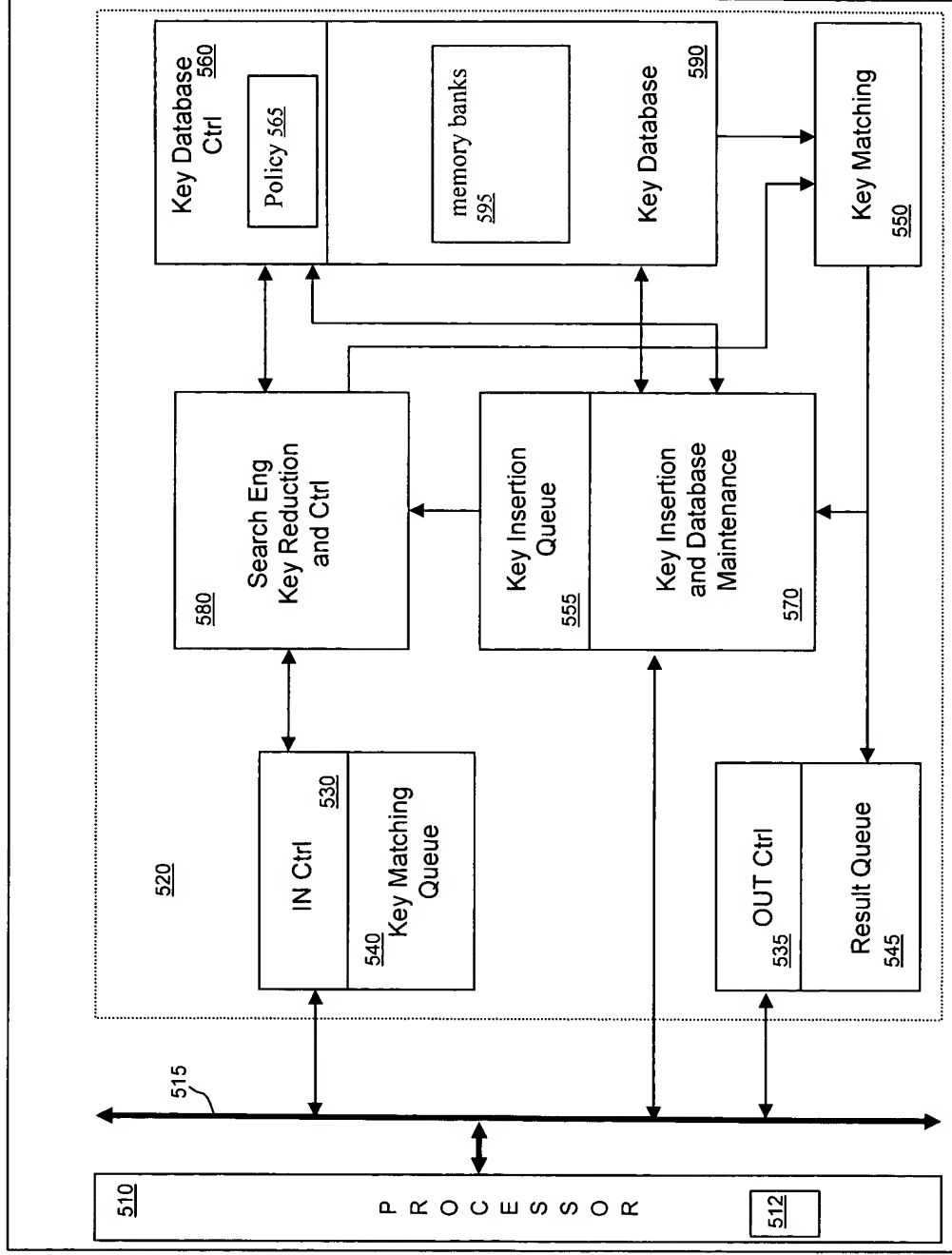


FIG. 5

600 →

	610A	610B	610C	610D	610E	610F
	Memory Bank I	Memory Bank II	Memory Bank III	Memory Bank IV	Memory Bank V	Memory Bank VI
620A—0	22347					
620B—1	15432	13245				
620G—2	756	29746	14576			
620D—3	9857	23587				
620E—4	27863		21984			
620F—5	17659					
620G—6	20793	11897				
620H—7	6754	10259				
620I—8						
620J—9	19873					
620K—10	3897	25673				
620L—11	3768					
620M—12	1325					

FIG. 6

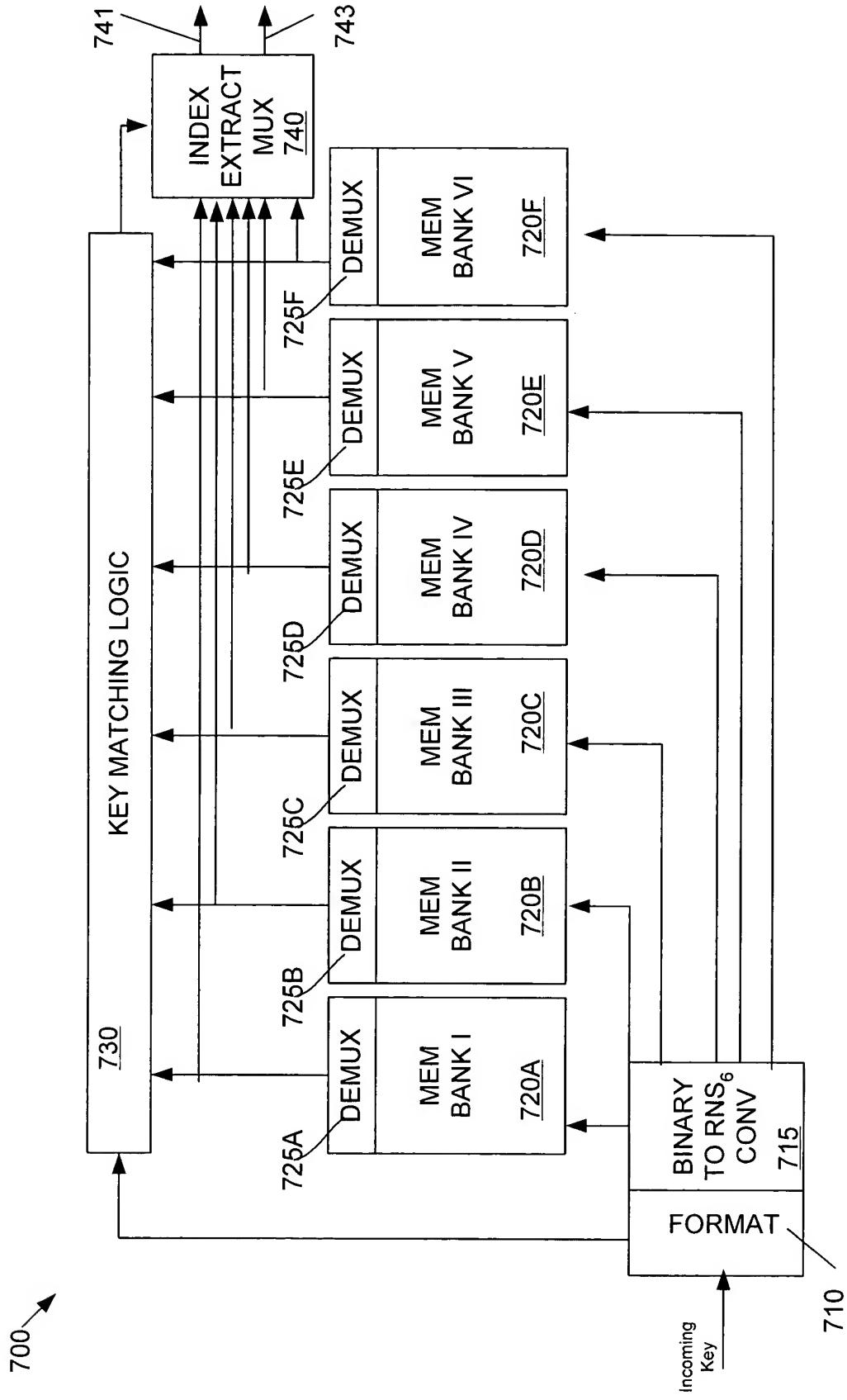


FIG. 7

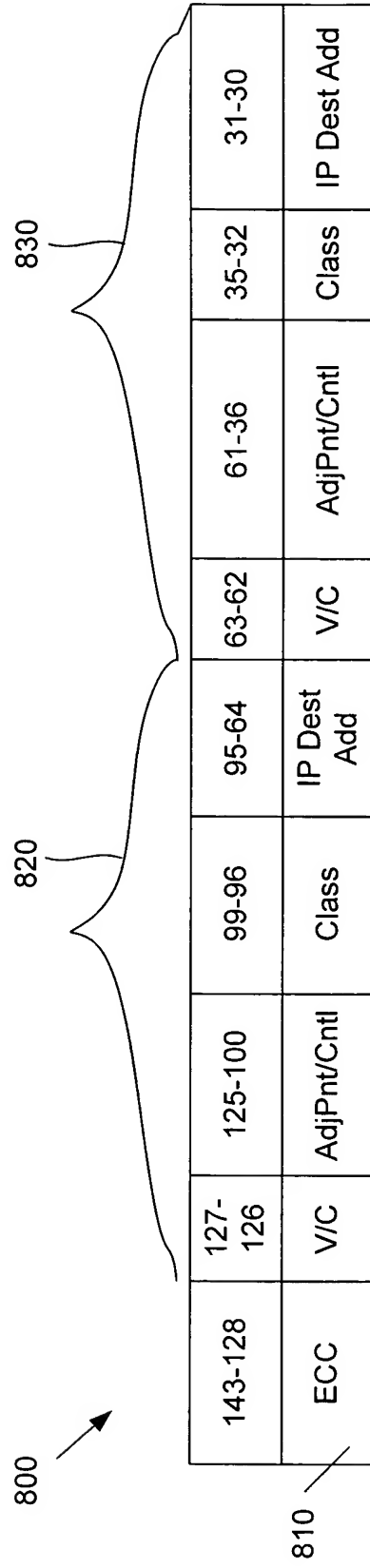


FIG. 8

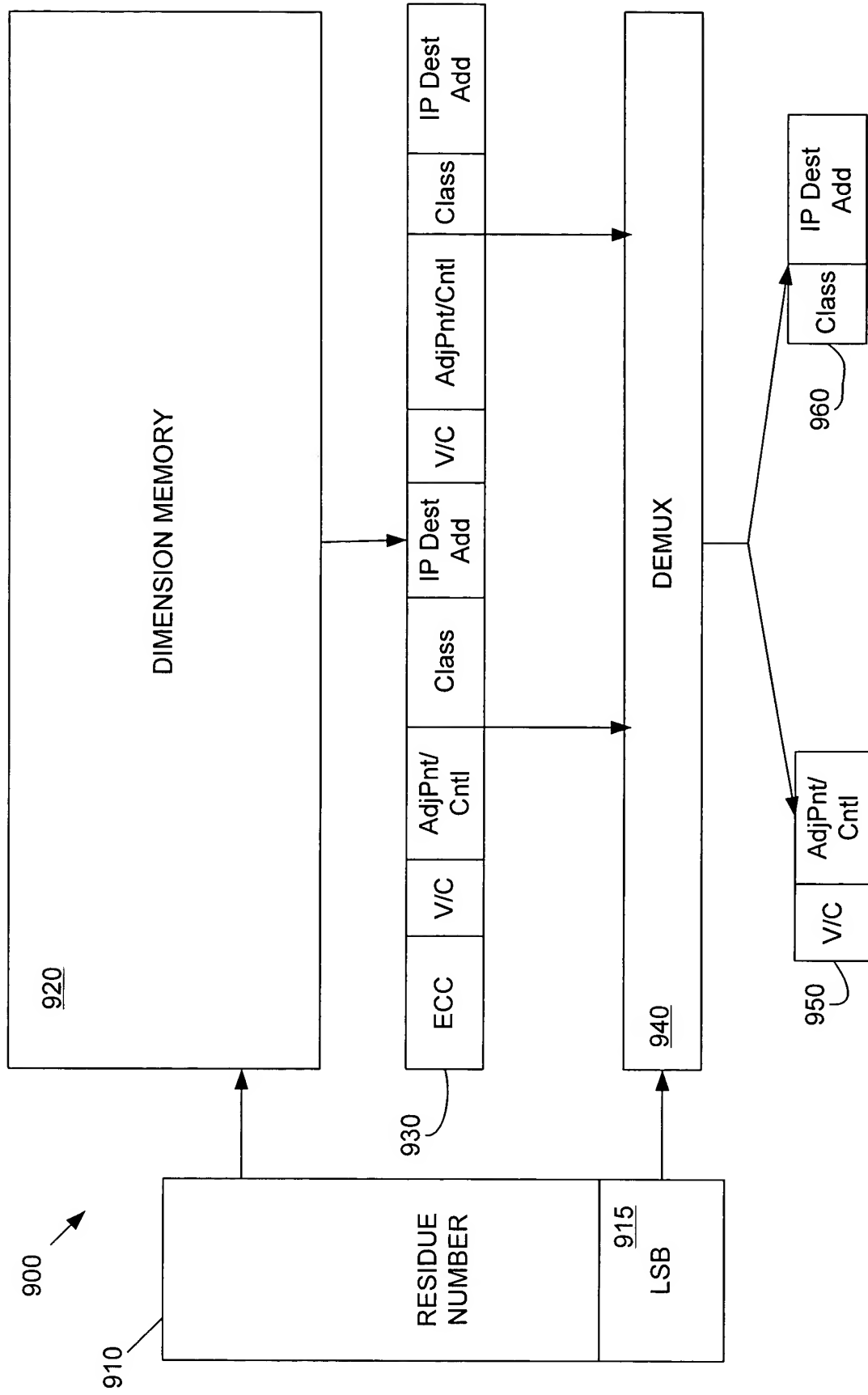


FIG. 9

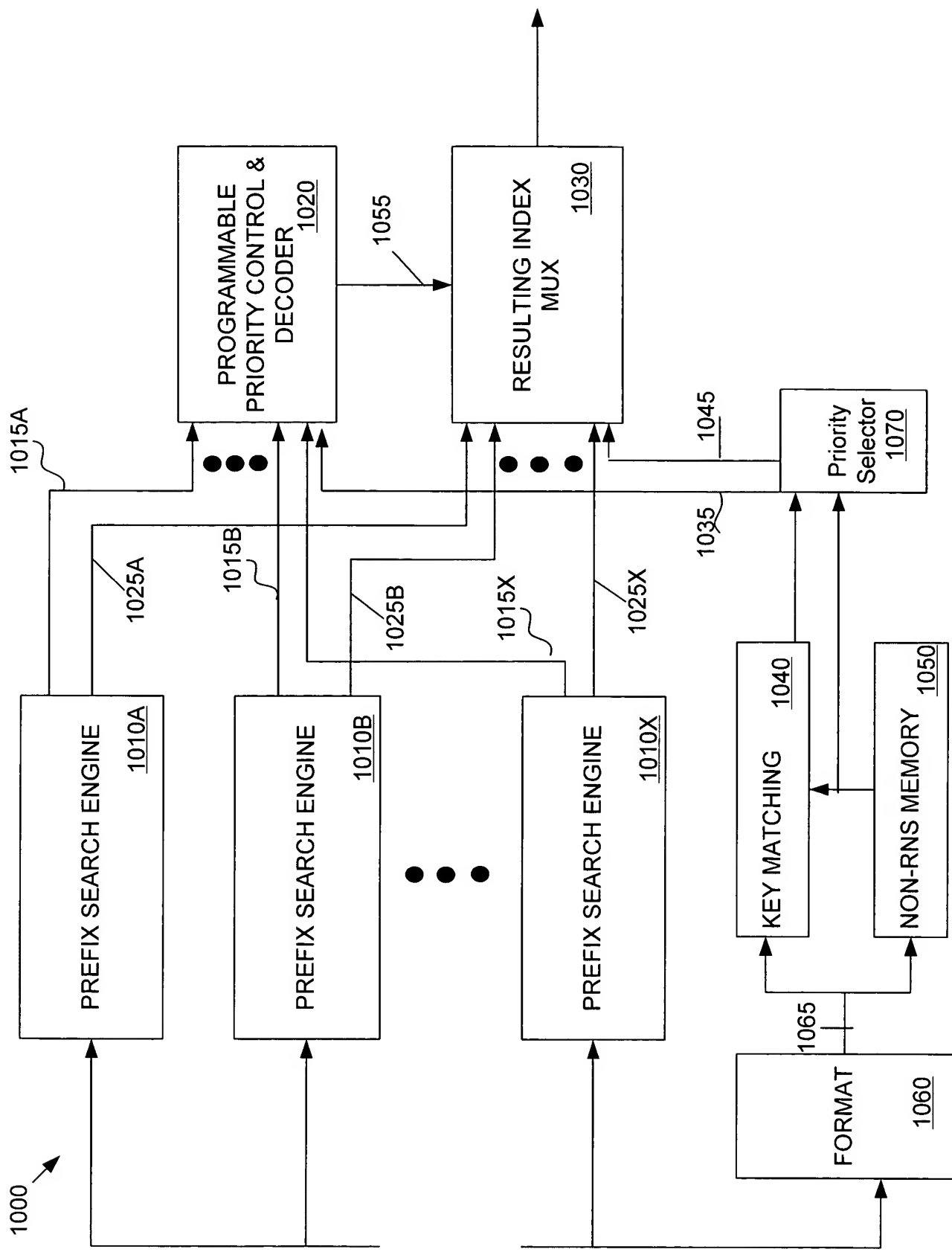


FIG. 10

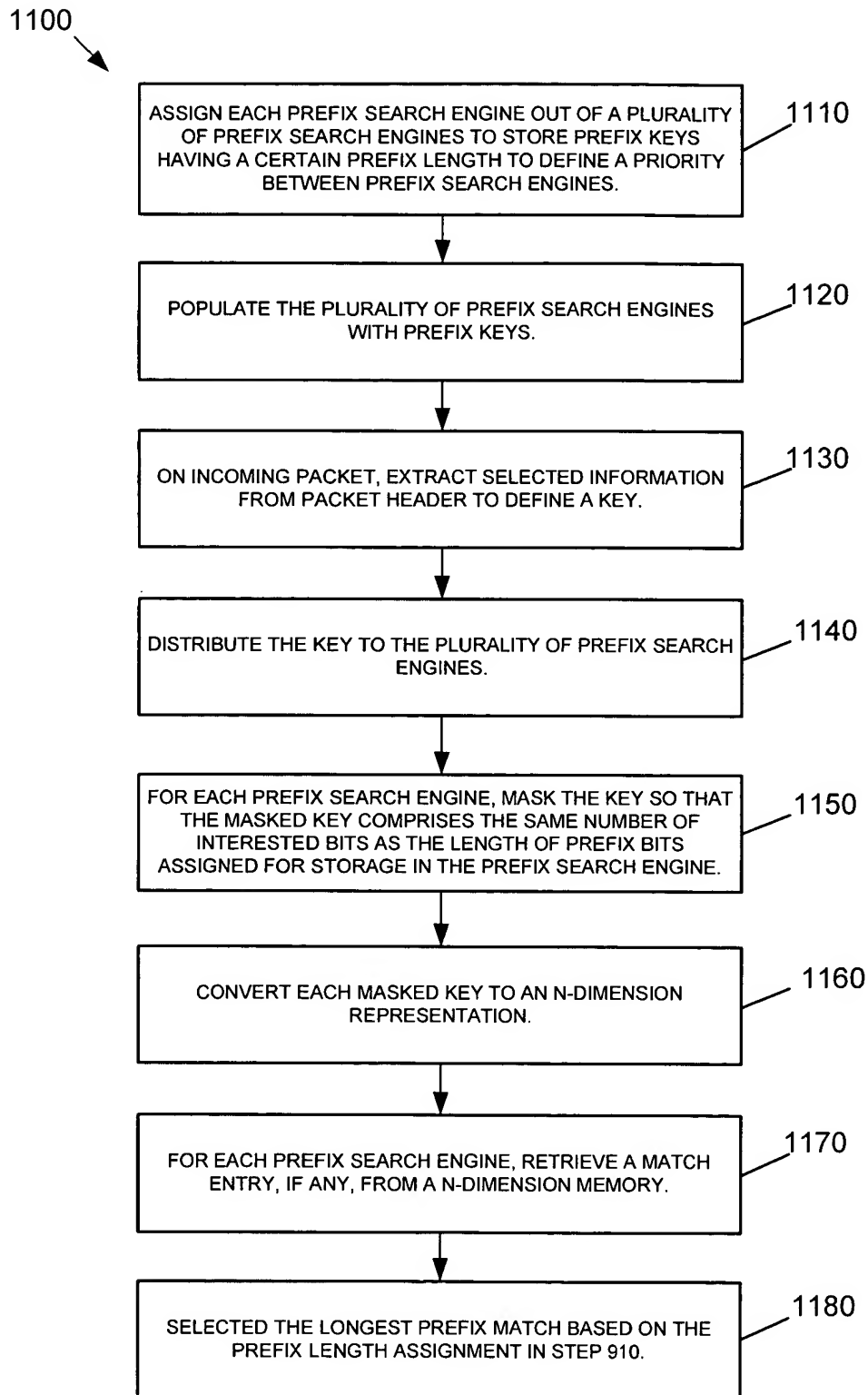


FIG. 11